



Attorney's Docket No.: 10559-312US1 / P9633

DT05 Rec'd PCT/PTO 28 OCT 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.

Art Unit : Unknown

Serial No. : 10/070,011

Examiner : Unknown

Filed : February 28, 2002

Title : MEMORY REFERENCE INSTRUCTIONS FOR MICRO ENGINE USED IN  
MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents

Washington, D.C. 20231

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the Filing Receipt for the above-referenced application to include the  
correct Priority data as follows:

"THIS APPLICATION IS A 371 OF PCT/US00/23993 08/31/2000

WHICH CLAIMS BENEFIT OF 60/151,961 09/01/1999"

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NOV 18 2002

Technology Center 2100

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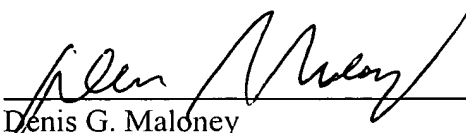
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Page : 2

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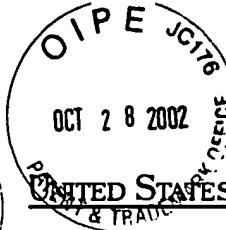
Respectfully submitted,

Date: 10/21/02

  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/070,011	07/03/2002	2183	1128	10559-312US1	11	36	2

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CONFIRMATION NO. 5760

FILING RECEIPT



\*OC000000008546183\*

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Applicant(s)

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DOCKETED BY PRACTICE SYSTEMS

Action Code Cor. OFA  
Base Date 8-2-02  
Due Date 9-2-02  
Deadline  
Initials REC On

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/US00/24095 09/01/2000  
WHICH CLAIMS BENEFIT OF 60/151,961-02/22/2000

09/01/1999

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Foreign Applications

Technology Center 2100

Projected Publication Date: Not Applicable, filed prior to November 29,2000

Non-Publication Request: No

Early Publication Request: No

Title

Docketed By Billing Secretary	
Due Date:	9-2-02
Deadline:	9-2-02
Initials:	RS

Memory reference instructions for micro engine used in multithreaded parallel processor

REPORTED  
TO INTEL

architecture

**Preliminary Class**

711

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